# **BCG002**



# 2W GaN Power Transistor

## 2W GaN Power Transistor (0.15µm x 480µm gate)

The BeRex BCG002 is a GaN Power HEMT die with a nominal 0.15 micron gate length and 480 micron gate width making the product ideally suited for amplifier applications where high-gain and high power from DC to 26 GHz. The product may be used in either wide-band or narrow-band applications. The BCG002 is produced using state of the art metallization with SI3N4 passivation and is screened to assure reliability.

#### **Product Features**

- 33.5 dBm Typical Saturated Output Power (P3dB) @ 12 GHz
- 12.5 dB Typical Saturated gain (G3dB) @ 12 GHz
- 55 % PAE Typical @ 12 GHz
- 0.15 X 480 Micron Recessed Gate

#### Applications

**Preliminary Datasheet** 

- Commercial
- Military / Hi-Rel.
- Test & Measurement



Chip dimensions : 500 X 520 microns Gate pad(GP) : 90 X 90 microns Drain pad(DP) : 150 X 90 microns Source pad(SP) : 90 X 90 microns Chip thickness : 75 microns

#### **Typical Performance**

SYMBOLS	PARAMETER/TEST CONDITIONS	TEST FRE- QUENCY	MIN.	TYPICAL	Max	UNIT
P3dB	Saturated Output Power @ P3dB (Vds = 28V, Id = 20mA)	12 GHz	32	33.5		dBm
G3dB	Power Gain @ P3dB (Vds = 28V, Id = 20mA)	12 GHz	11.0	12.5		dB
PAE	PAE @ P3dB (Vds = 28V, Id = 20mA)	12 GHz		55		%
I <sub>dss</sub>	Saturated Drain Current ( $V_{gs}$ = 0.0 V, $V_{ds}$ = 10.0 V)			330	400	mA
V <sub>p</sub>	Pinch-off Voltage ( $I_{ds}$ = 0.48 mA, $V_{ds}$ = 10 V)			-1.9		V
$BV_{gd}$	Drain Breakdown Voltage (I <sub>g</sub> = 0.48 mA, source open)			84		V
BV <sub>gs</sub>	Source Breakdown Voltage (I <sub>g</sub> = 0.48 mA, drain open)			-6.5		V
R <sub>th</sub>	Thermal Resistance			10.1		° C/W

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#### MAXIMUM RATING (Ta = 25° C)

SYMBOLS	PARAMETERS	ABSOLUTE
$V_{ds}$	Drain-Source Voltage	90 V
$V_{gs}$	Gate-Source Voltage	-10 V
I <sub>ds</sub>	Drain Current	0.6 A
$I_{gsf}$	Forward Gate Current	2 mA
$T_{stg}$	Storage Temperature	-60° C to 150° C
Pt	Total Power Dissipation	4.0 W

Exceeding any of the above Maximum Ratings will result in reduced MTTF and may cause permanent damage to the device.

FREQ.	S11	S11	S21	S21	S12	S12	S22	S22
[GHZ]	[MAG]	[ANG.]	[MAG]	[ANG.]	[MAG]	[ANG.]	[MAG]	[ANG.]
1	0.94	-35.35	10.19	154.50	0.032	77.42	0.79	-18.19
2	0.86	-67.92	8.98	133.53	0.058	48.62	0.69	-31.85
3	0.78	-97.37	7.72	115.14	0.069	40.83	0.61	-43.21
4	0.72	-122.19	6.52	99.91	0.080	28.00	0.52	-51.25
5	0.69	-146.78	5.58	86.74	0.083	24.45	0.46	-56.97
6	0.68	-167.26	4.76	74.90	0.078	13.98	0.40	-62.01
7	0.68	175.73	4.14	64.40	0.080	13.86	0.36	-66.81
8	0.69	160.90	3.67	54.66	0.079	8.18	0.33	-73.48
9	0.72	146.97	3.23	45.61	0.077	7.40	0.30	-78.23
10	0.74	135.07	2.82	36.80	0.074	2.42	0.27	-83.05
11	0.76	126.53	2.49	28.84	0.065	3.47	0.24	-90.68
12	0.78	119.24	2.25	22.06	0.068	4.86	0.23	-101.92
13	0.80	112.54	2.03	15.01	0.064	-3.01	0.22	-119.04
14	0.82	105.15	1.84	7.21	0.065	1.19	0.23	-132.81
15	0.84	97.25	1.63	0.08	0.061	-0.31	0.26	-143.32
16	0.87	91.06	1.46	-7.15	0.067	-2.02	0.28	-152.25
17	0.88	87.26	1.30	-12.34	0.061	-1.31	0.32	-159.92
18	0.88	84.50	1.15	-17.67	0.071	-1.51	0.35	-166.27
19	0.90	83.85	1.04	-21.74	0.066	2.91	0.39	-171.72
20	0.90	83.91	0.92	-25.56	0.063	5.89	0.42	-175.93
21	0.92	82.98	0.83	-28.34	0.069	6.42	0.46	-179.06
22	0.93	82.24	0.75	-31.46	0.074	7.68	0.46	176.96
23	0.95	82.68	0.69	-35.31	0.075	8.57	0.49	172.71
24	0.94	80.51	0.64	-39.05	0.086	7.54	0.48	167.26
25	0.92	83.52	0.59	-41.00	0.079	9.90	0.51	158.35
26	0.90	83.86	0.55	-43.03	0.086	9.72	0.55	153.75

#### S-PARAMETERS (Vds = 28V, Ids = 20mA, T = 25°C)

**Preliminary Datasheet** 

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PIN\_POUT / Gain, PAE (@ 12 GHz)



#### WIRE BONDING INFORMATION

Always follow wire bonding diagrams recommended by BeRex for each device to achieve optimum device performance and reliability. As a general rule, bonding temperature should be kept to a maximum of 280°C for no longer than 2 minutes for all bonding wires.



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## **ESD** Rating

ESD Testing Mode	Reference (Current Revision)	Resulting Classification		
ESD - HBM	JDS - 001 - 2017	Class1A (Passes <500V)		



## HANDLING PRECAUTIONS

GaN HEMTs are very sensitive to and may be damaged by Electrostatic Discharge (ESD). Therefore, proper ESD precautions must be taken whenever you are handling these devices. It is critically important that all work surfaces, and assembly equipment, as well as the operator be properly grounded when handling these devices to prevent ESD damage.

#### **DIE ATTACH RECOMMENDATIONS**

BeRex recommends the "Eutectic" die attach using Au/Sn (80/20) pre-forms. The die attach station must have accurate temperature control, and the operation should be performed with parts no hotter than 300°C for less than 10 seconds. An inert forming gas (90%  $N_2/10\%$   $H_2$ ) or clean, dry  $N_2$  should be used.

Use of conductive epoxy (gold or silver filled) may also be acceptable for die-attaching low power devices.

#### **SHIPPING & STORAGE**

BeRex's standard chip device shipping package consists of an antistatic "Gel-Pak", holding the chips, placed inside a sealed metallized bag. This packaging is designed to provide a reasonable measure of protection from both mechanical and ESD damage.

Chip devices should be stored in a clean, dry Nitrogen gas environment at room temperature until they are required for assembly. Only open the shipping package or perform die assembly in a work area with a class 10,000 or better clean room environment to prevent contamination of the exposed devices.



# CAUTION

THIS PRODUCT CONTAINS GALLIUM NITRIDE (GaN) ON SILICON CARBIDE (SIC) WHICH CAN BE HAZARDOUS TO THE HUMAN BODY AND THE ENVIRONMENT. THEREFORE, IT MUST BE HANDLED WITH CARE AND IN ACCORDANCE WITH ALL GOVERNMENTAL AND COMPANY REGULATIONS FOR THE SAFE HANDLING AND DISPOSAL OF HAZARDOUS WASTE. DO NOT BURN, DESTROY, CUT, CRUSH OR CHEMICALLY DISSOLVE THE PRODUCT. DO NOT LICK THE PROD-UCT OR IN ANY WAY ALLOW IT TO ENTER THE MOUTH. EXCLUDE THE PRODUCT FROM GENERAL INDUSTRIAL WASTE OR GARBAGE AND DISPOSE OF ONLY IN ACCORDANCE TO APPLICABLE LAWS AND/OR ORDINANCES.

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